

MacPherson Kwok Chen & Heid LLP

17611 U.S. PTO  
022004  
17611 U.S. PTO  
022004  
FEB 23 2004  
MAIL DATE CANCELLED  
TRADE MARK OFFENSE

1762 Technology Drive, Suite 226

2402 Michelson Drive, Suite 210

San Jose, CA 95110

Irvine, CA 92612

Tel. (408) 392-9250

Tel. (949) 752-7040

Fax (408) 392-9262

Fax (949) 752-7049

Email: [gmichelson@macpherson-kwok.com](mailto:gmichelson@macpherson-kwok.com)

[www.macpherson-kwok.com](http://www.macpherson-kwok.com)

Atty. Docket No.: M-15311 US

February 20, 2004

Mail Stop Patent Application  
Commissioner For Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Enclosed herewith for filing is a patent application, as follows:

Inventor(s): Om P. Agrawal; Howard Tang; Jack Wong

Title: Upgradeable And Reconfigurable Programmable Logic Device

- ☒ Return Receipt Postcard  
☒ This Transmittal Letter (in duplicate)  
☒ Eighteen (18) page(s) Specification (not including claims)  
☒ Nine (9) page(s) Claims  
☒ One (1) page Abstract  
☒ Three (3) Sheet(s) of Informal Drawings including Figures 1, 2, 3, and 4  
☒ Submission of Formal Drawings with three (3) sheets of drawings including Figures 1, 2, 3, and 4  
☒ Two (2) page(s) Declaration For Patent Application and Power of Attorney  
☒ Three (3) page(s) Assignment and Recordation Form Cover Sheet  
☒ One (1) page Non-Publication Request  
☒ Two (2) page(s) Information Disclosure Statement  
☒ PTO-1449 with five (5) references

17354 U.S. PTO  
10/783886  
022004

CLAIMS AS FILED

For	Number			Number		Rate		Basic Fee
	<u>Filed</u>			<u>Extra</u>				
Total Claims	35	-20	=	15	X	\$18.00	=	\$ 270.00
Independent Claims	5	-3	=	2	X	\$86.00	=	\$ 172.00
<input type="checkbox"/>	Fee of _____ for the first filing of one or more multiple dependent claims per application							\$
<input type="checkbox"/>	Fee for Request for Extension of Time							\$

Please make the following charges to Deposit Account No. 50-2257:

- ☒ Total fee for filing the patent application in the amount of \$ 1,212.00  
☒ The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment to Deposit Account No. 50-2257.

EXPRESS MAIL LABEL NO:

EV411212055US

Respectfully submitted,

*Greg J. Michelson*

Greg J. Michelson  
Attorney for Applicants  
Reg. No. 44,940

EXPRESS MAIL LABEL NO:  
EV411212055US

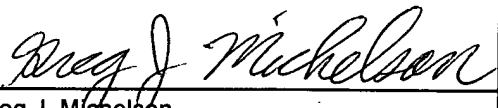
MODIFIED PTO/SB/35 (11-00)

**REQUEST AND CERTIFICATION  
UNDER  
35 U.S.C. 122(b)(2)(B)(i)**

Inventors	Om P. Agrawal; Howard Tang; Jack Wong	
Title	Upgradeable And Reconfigurable Programmable Logic Device	
Atty Docket Number	M-15311 US	

I hereby certify that the invention disclosed in the attached application **has not and will not be** the subject of an application filed in another country, or under a multilateral agreement, that requires publication at eighteen months after filing. I hereby request that the attached application not be published under 35 U.S.C. 122(b).

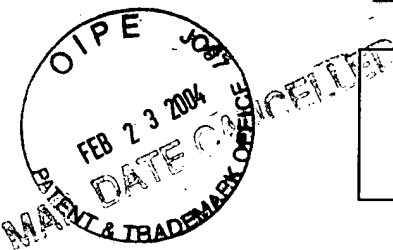
February 20, 2004  
Date

  
Greg J. Michelson  
Attorney for Applicants  
Reg. No.: 44,940

This request must be signed in compliance with 37 CFR 1.33(b) and submitted with the application **upon filing**.

Applicant may rescind this nonpublication request at any time. If applicant rescinds a request that an application not be published under 35 U.S.C. 122(b), the application will be scheduled for publication at eighteen months from the earliest claimed filing date for which a benefit is claimed.

If applicant subsequently files an application directed to the invention disclosed in the attached application in another country, or under a multilateral international agreement, that requires publication of applications eighteen months after filing, the applicant **must** notify the United States Patent and Trademark Office of such filing within forty-five (45) days after the date of the filing of such foreign or international application. **Failure to do so will result in abandonment of this application (35 U.S.C. 122(b)(2)(B)(iii)).**



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Om P. Agrawal; Howard Tang; Jack Wong  
Assignee: Lattice Semiconductor Corporation  
Title: Upgradeable And Reconfigurable Programmable Logic Device  
Serial No.: Unknown Filing Date: Herewith  
Examiner: Unassigned Group Art Unit: Unknown  
Docket No.: M-15311 US

Irvine, California  
February 20, 2004

Attn: Official Draftsperson  
COMMISSIONER FOR PATENTS  
Alexandria, VA 22313-1450

**SUBMISSION OF FORMAL DRAWINGS**

Dear Sir:

Applicants submit three (3) sheets of formal drawings, consisting of Figures 1, 2, 3, and 4, in the above-named application. If there are any questions regarding these drawings, please call the undersigned at (949) 752-7040.

Express Mail #EV411212055US

Respectfully submitted,



Greg J. Michelson  
Attorney for Applicants  
Reg. No. 44,940

LAW OFFICES OF  
MacPHERSON KWOK CHIEN  
& HEID LLP

2402 MICHELSON DRIVE  
SUITE 210  
IRVINE, CA 92612  
(949) 752-7040  
FAX (949) 752-7049